

FIG. 1

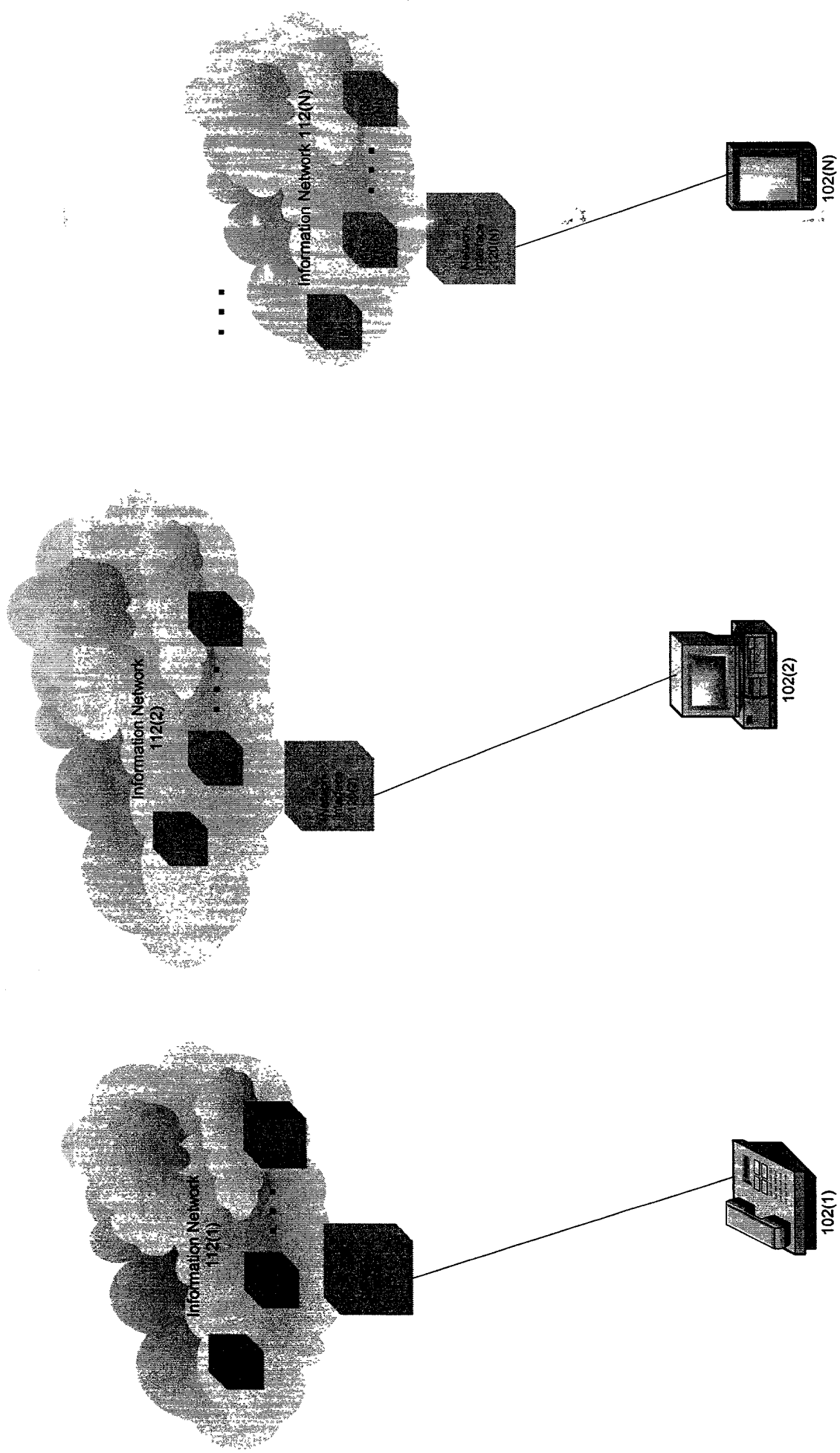


FIG. 1

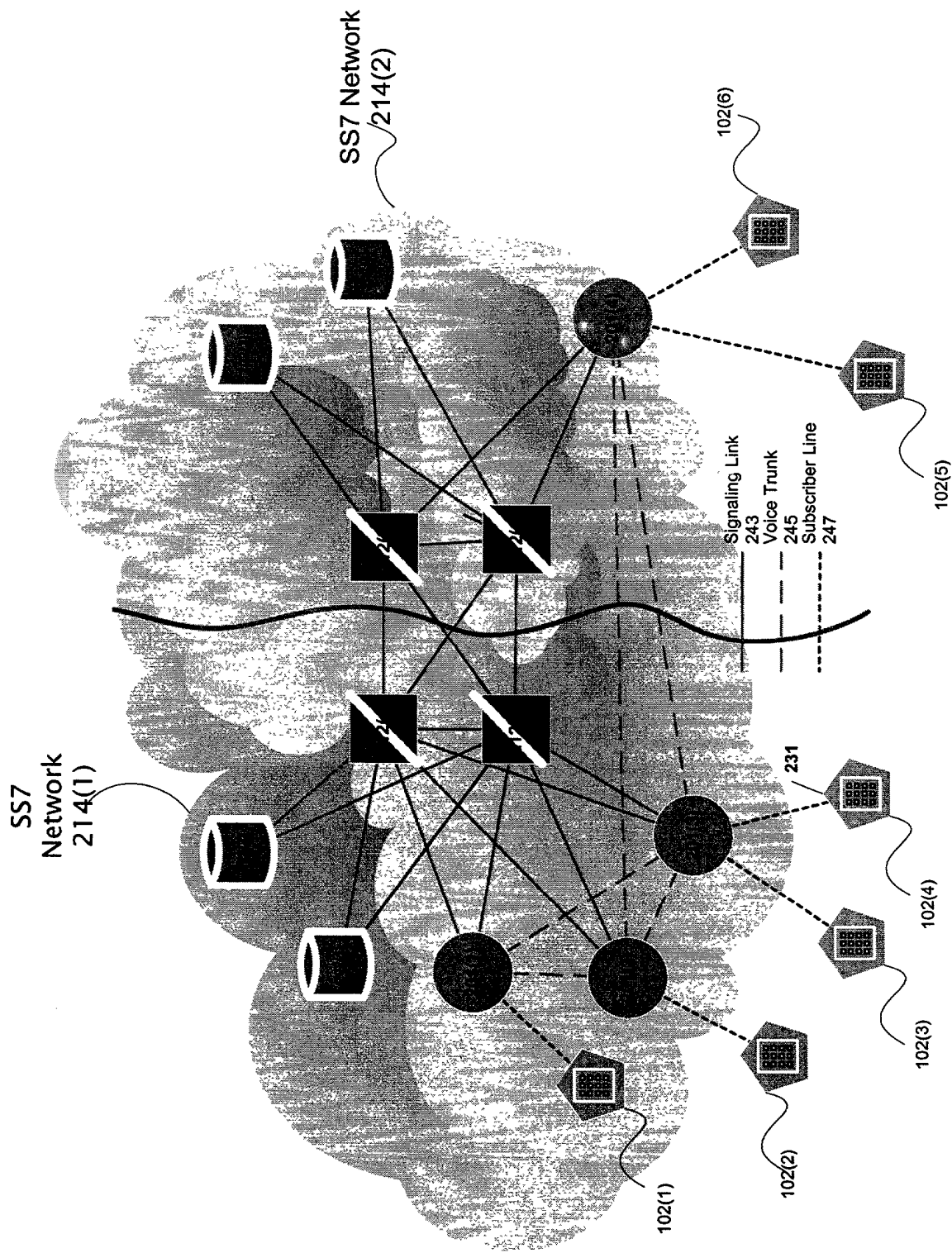


FIG. 2

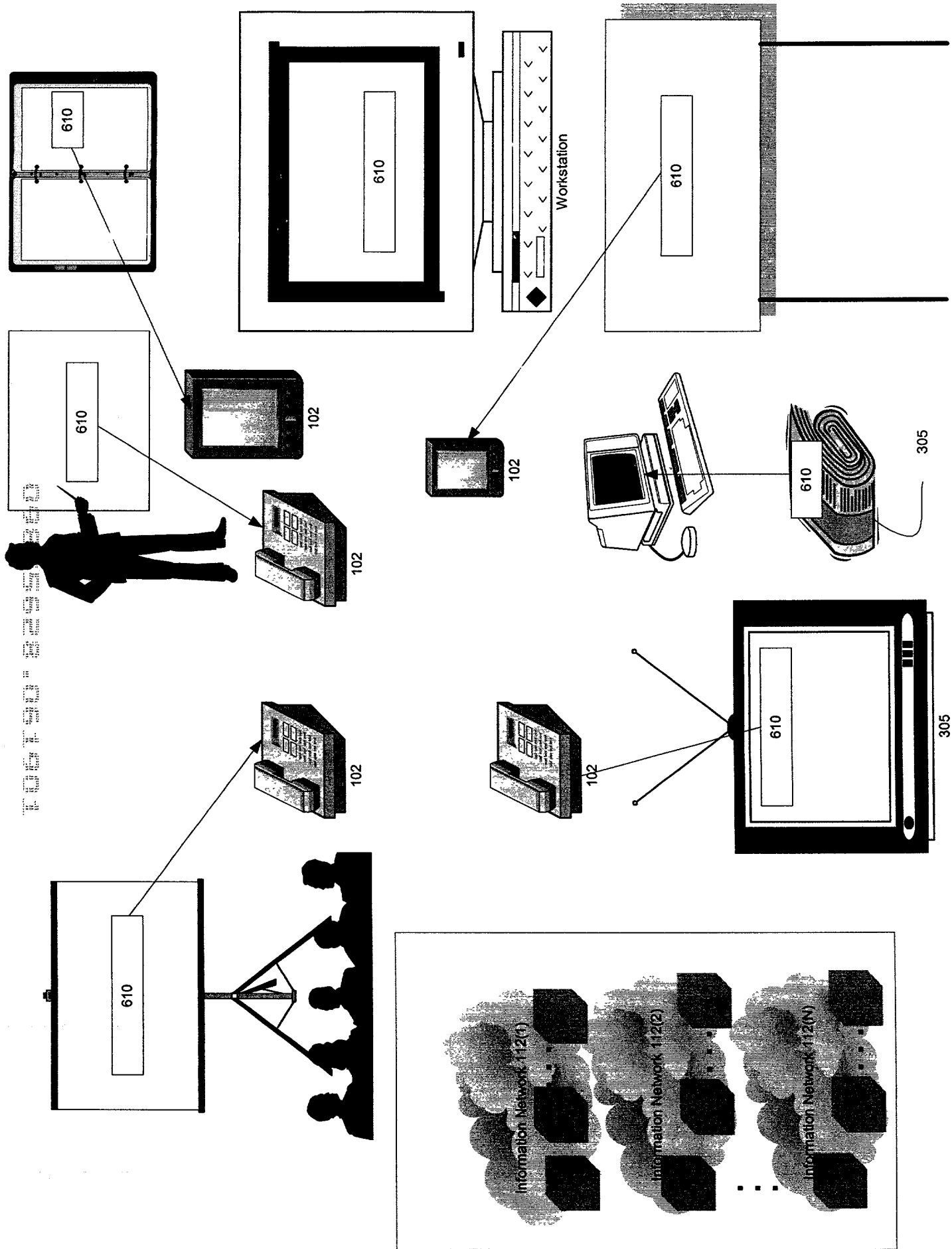


FIG. 3

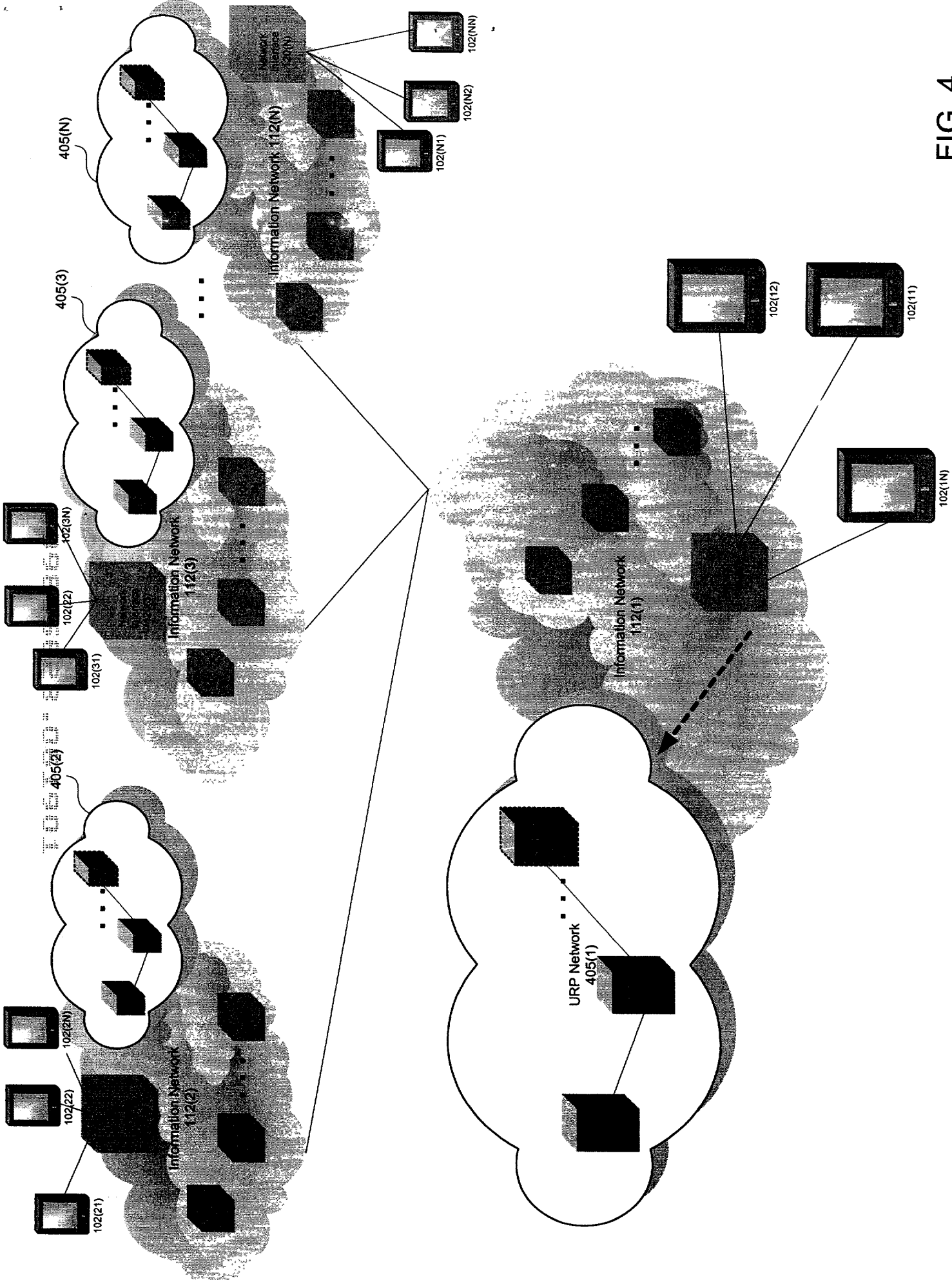


FIG. 4

FIG. 5 is a schematic diagram of a network architecture.

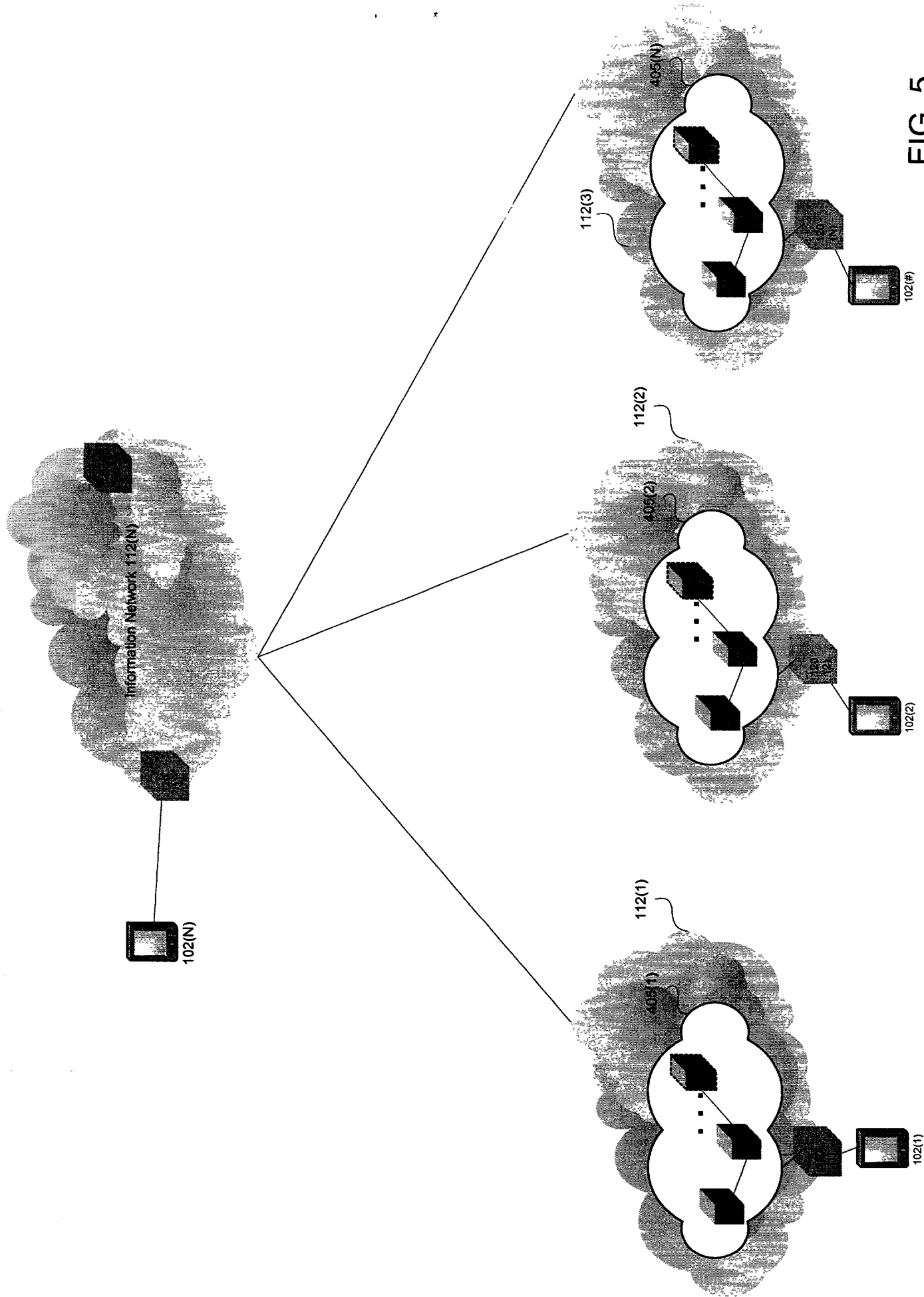


FIG. 5

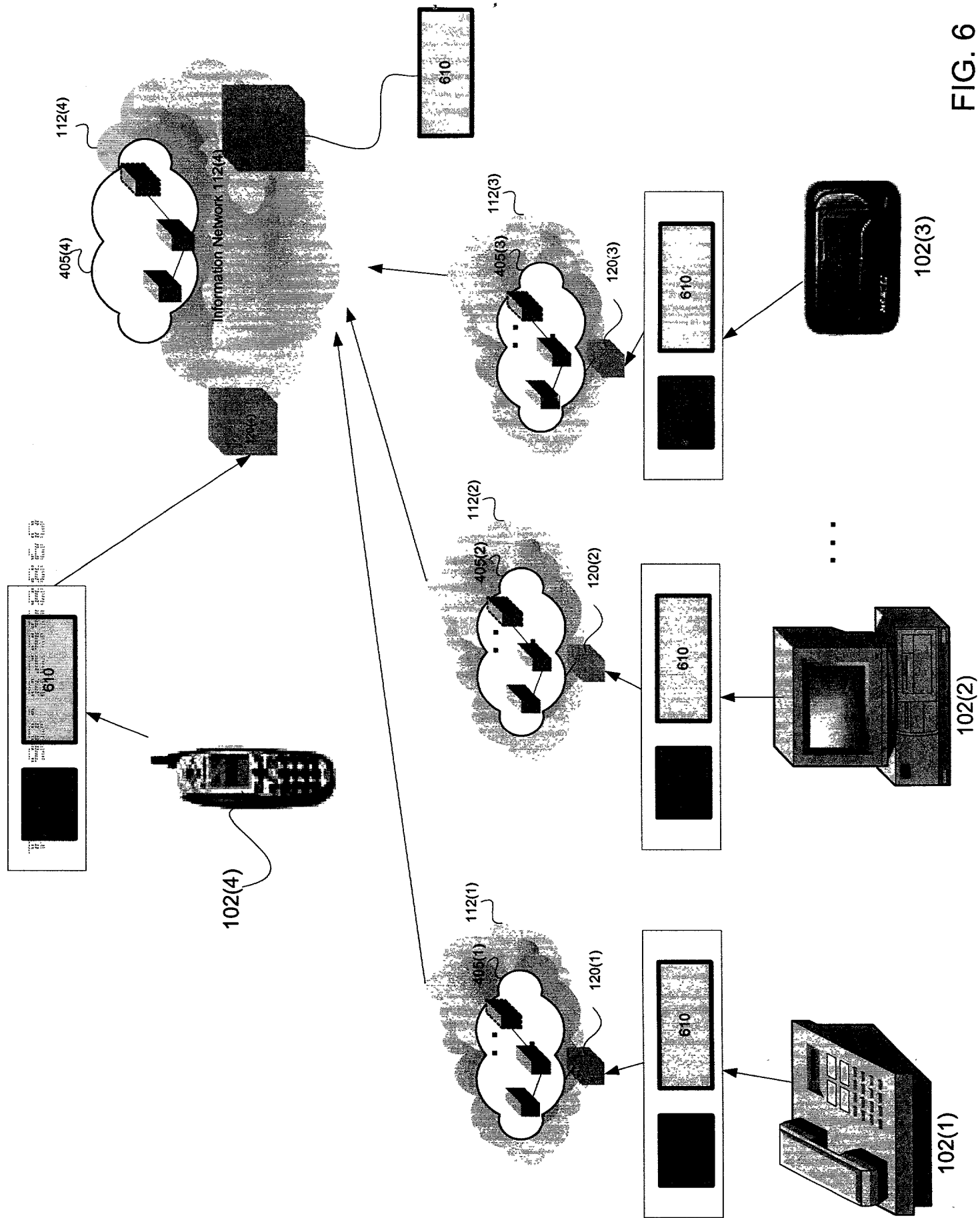


FIG. 6

FIG. 7 is a schematic diagram of a device 700, which includes a display 705 and a sensor 710.

705

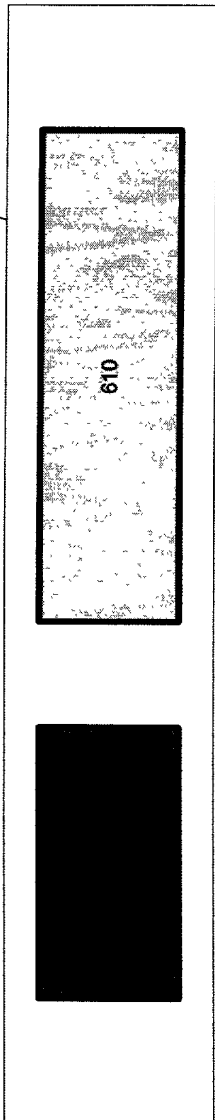


FIG. 7

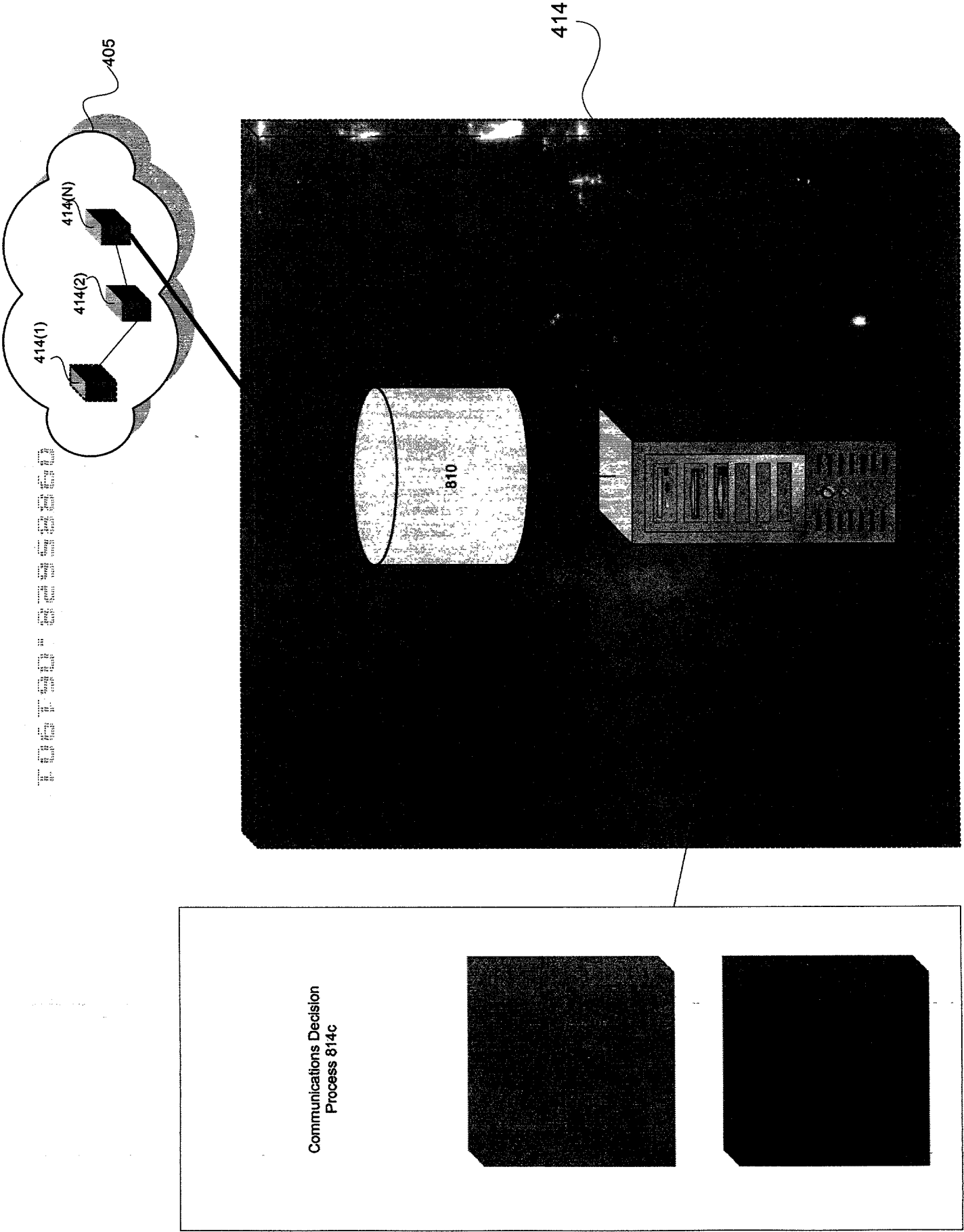


FIG. 8



FIG. 9 is a block diagram of a system architecture for network initialization.

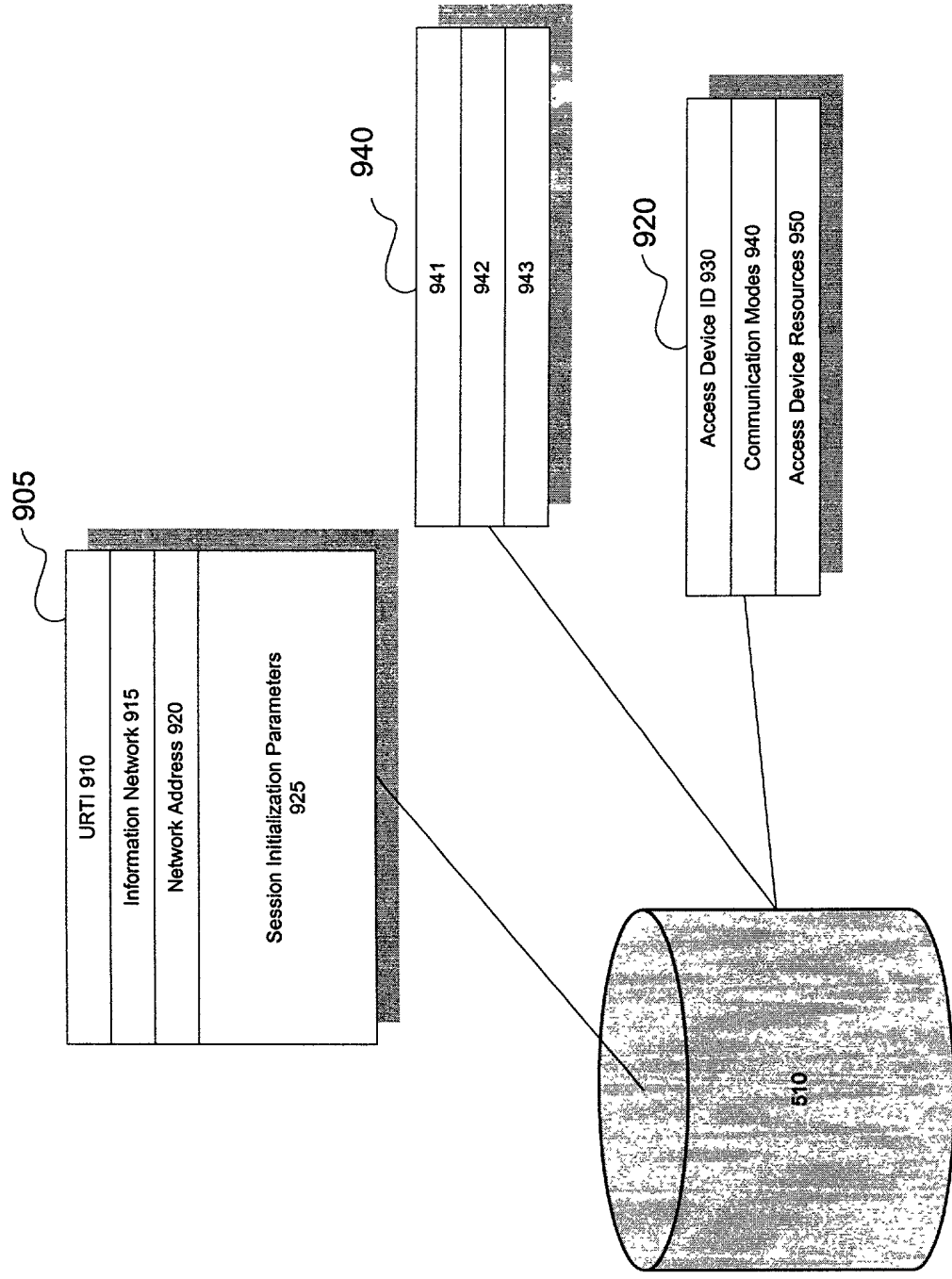


FIG. 9

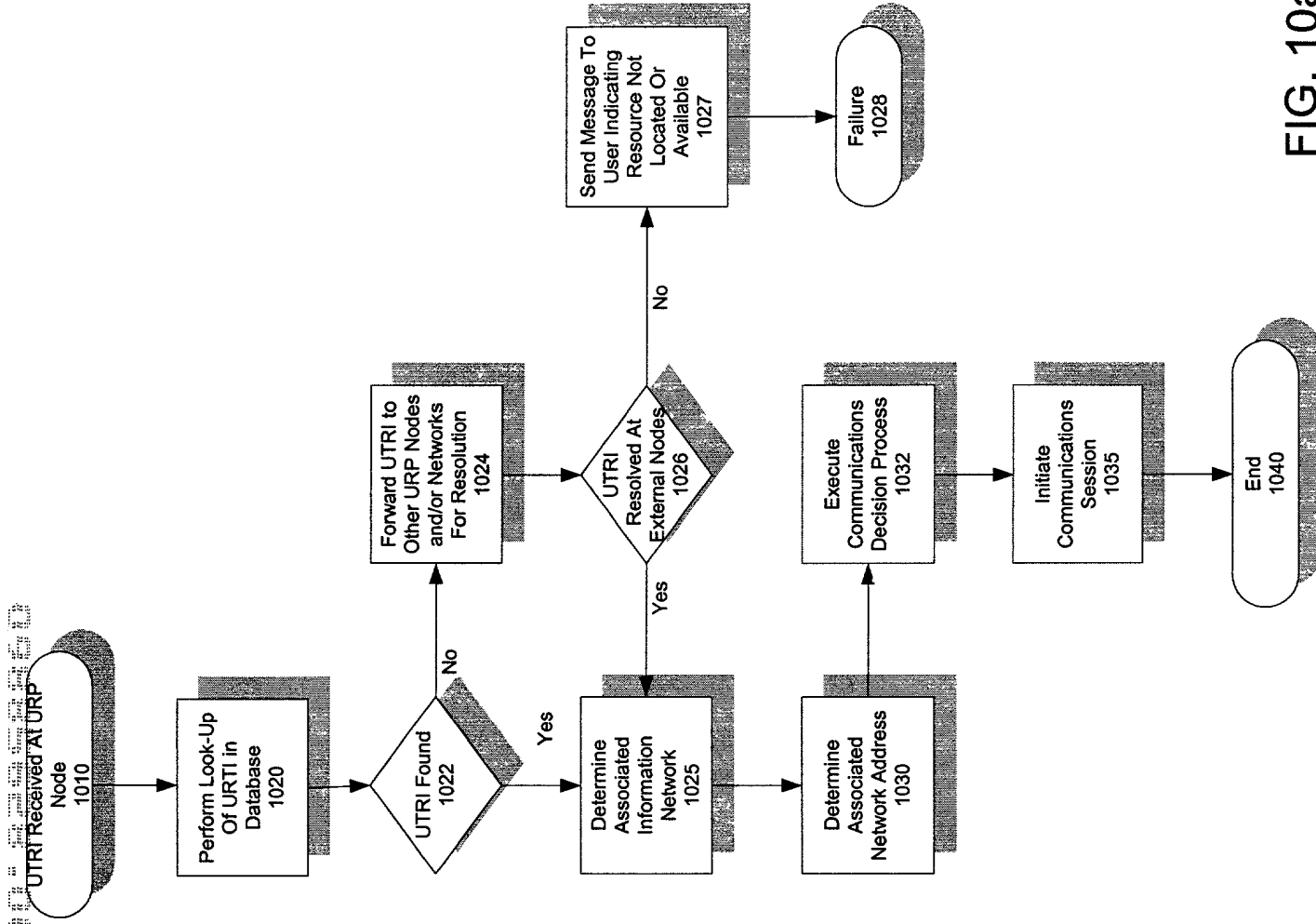


FIG. 10a

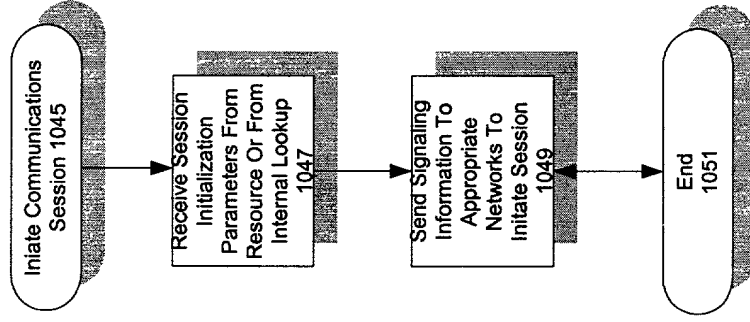


FIG. 10b

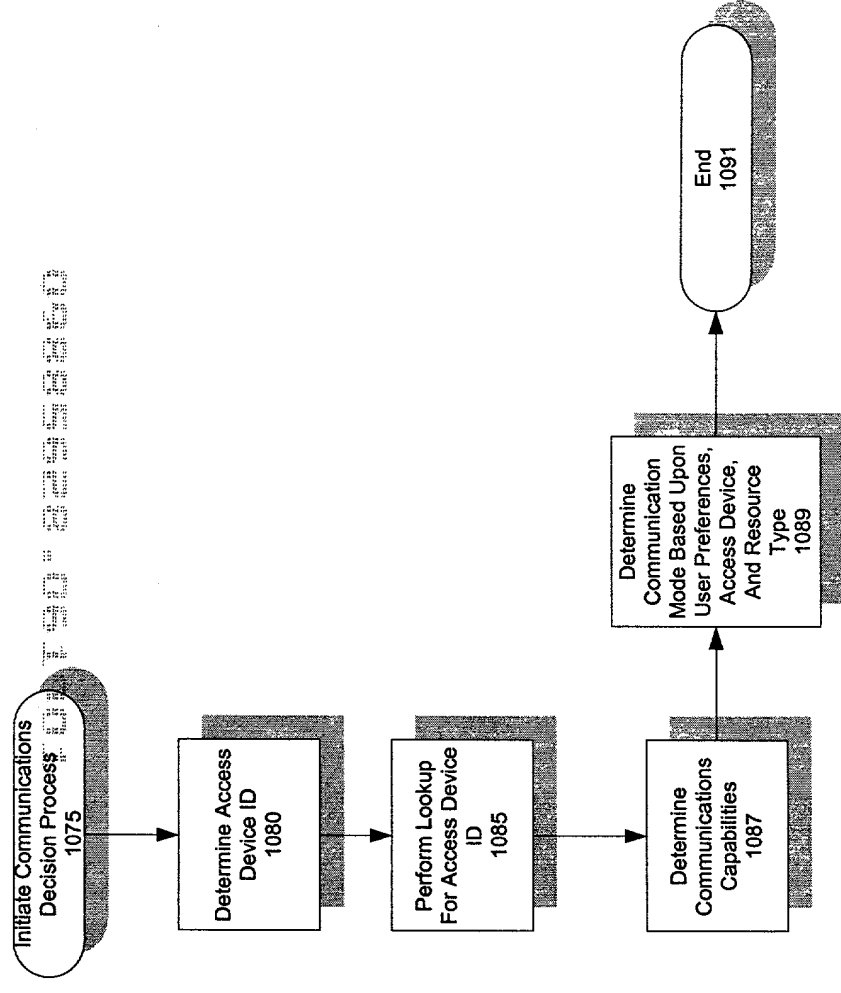


FIG. 10c

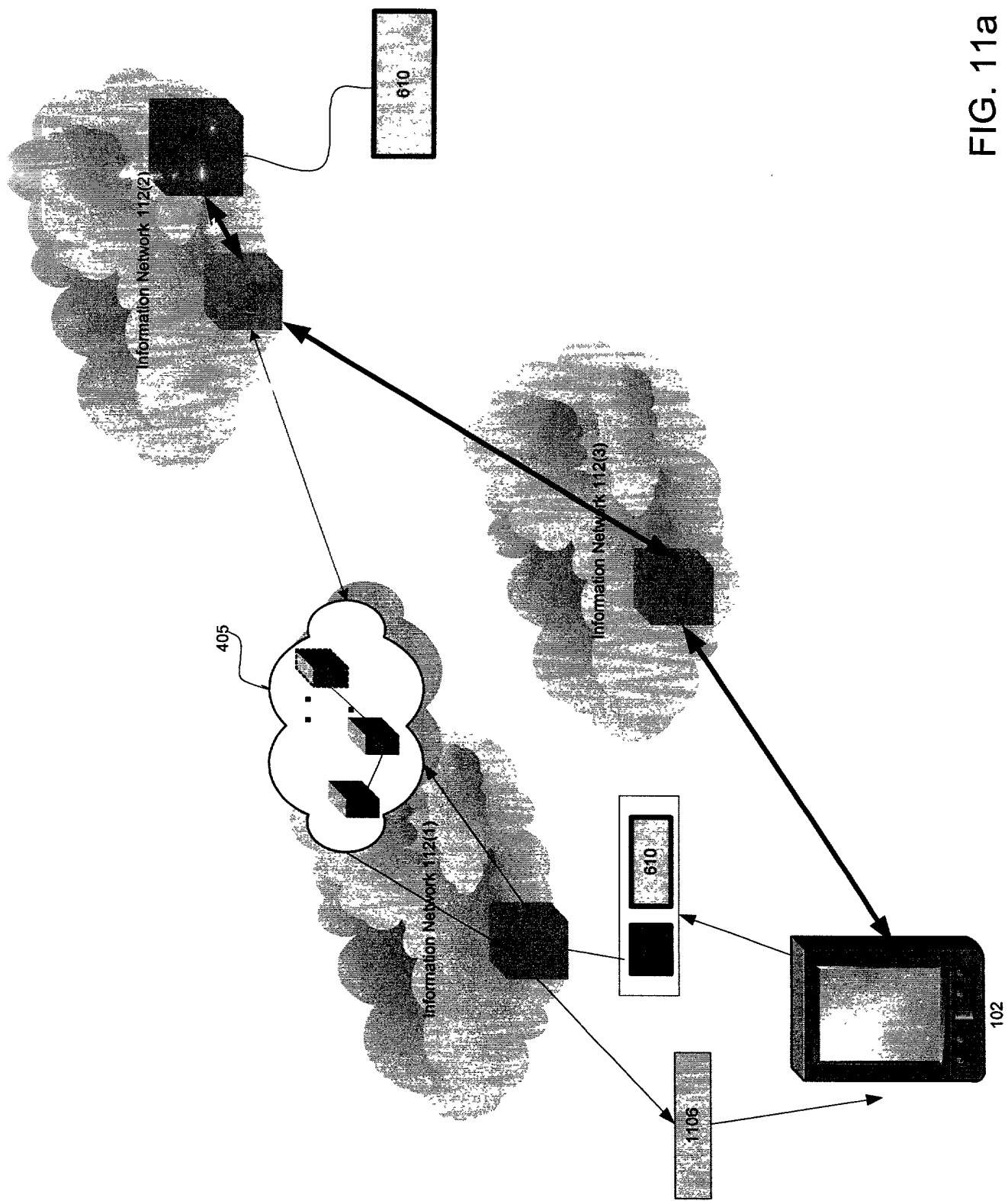


FIG. 11a

FIG. 11b is a schematic diagram of a network architecture.

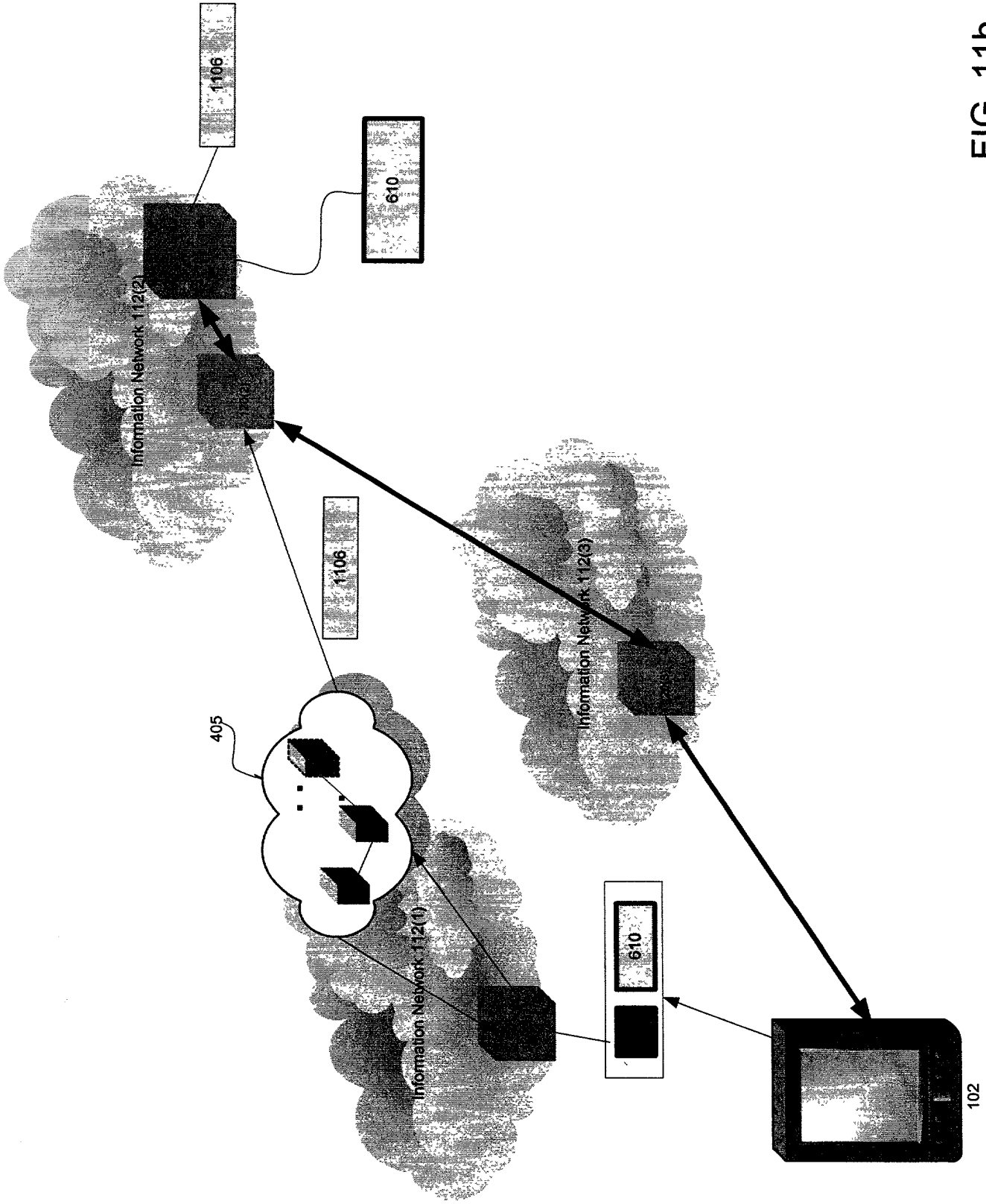


FIG. 11b

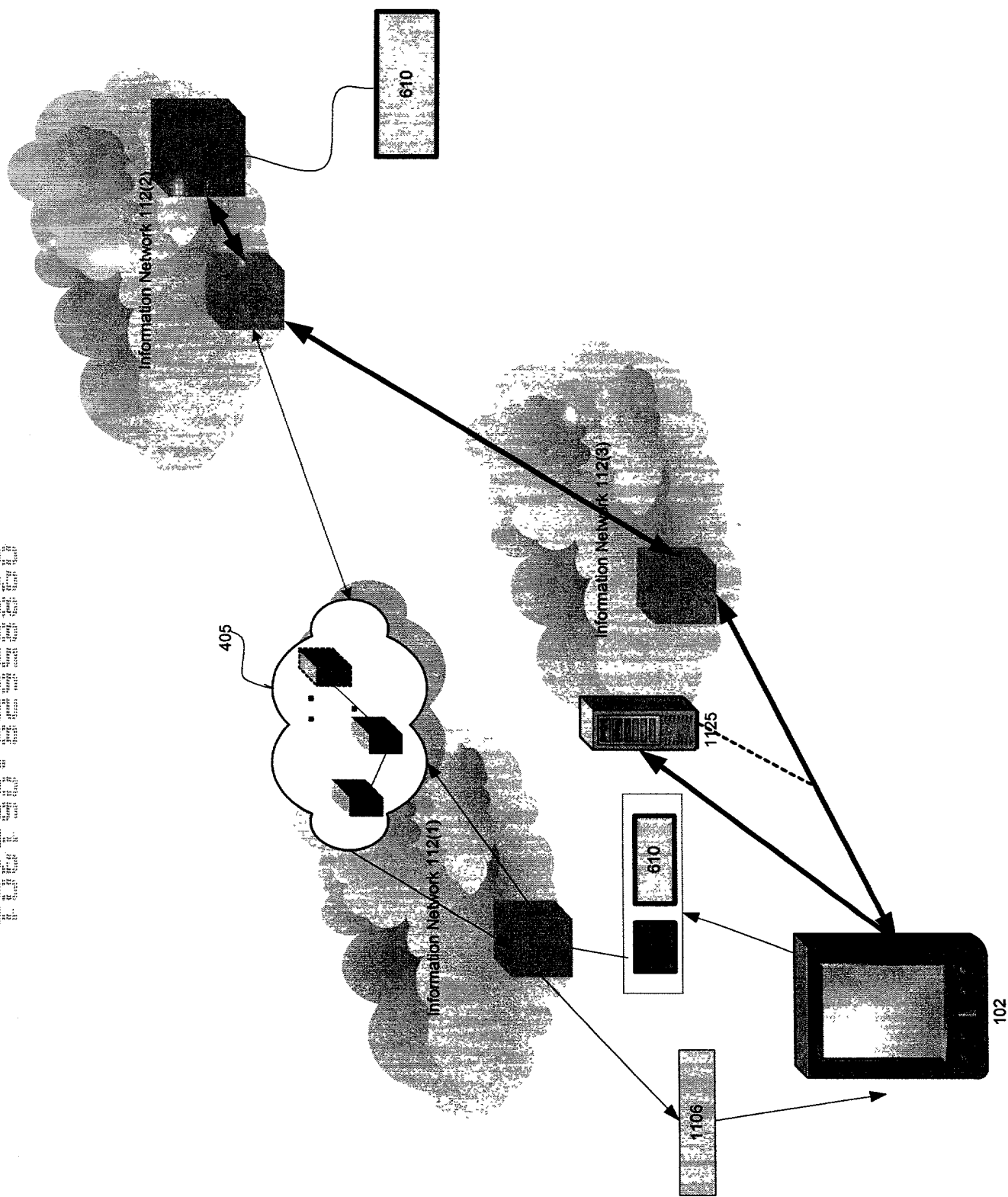


FIG. 11c

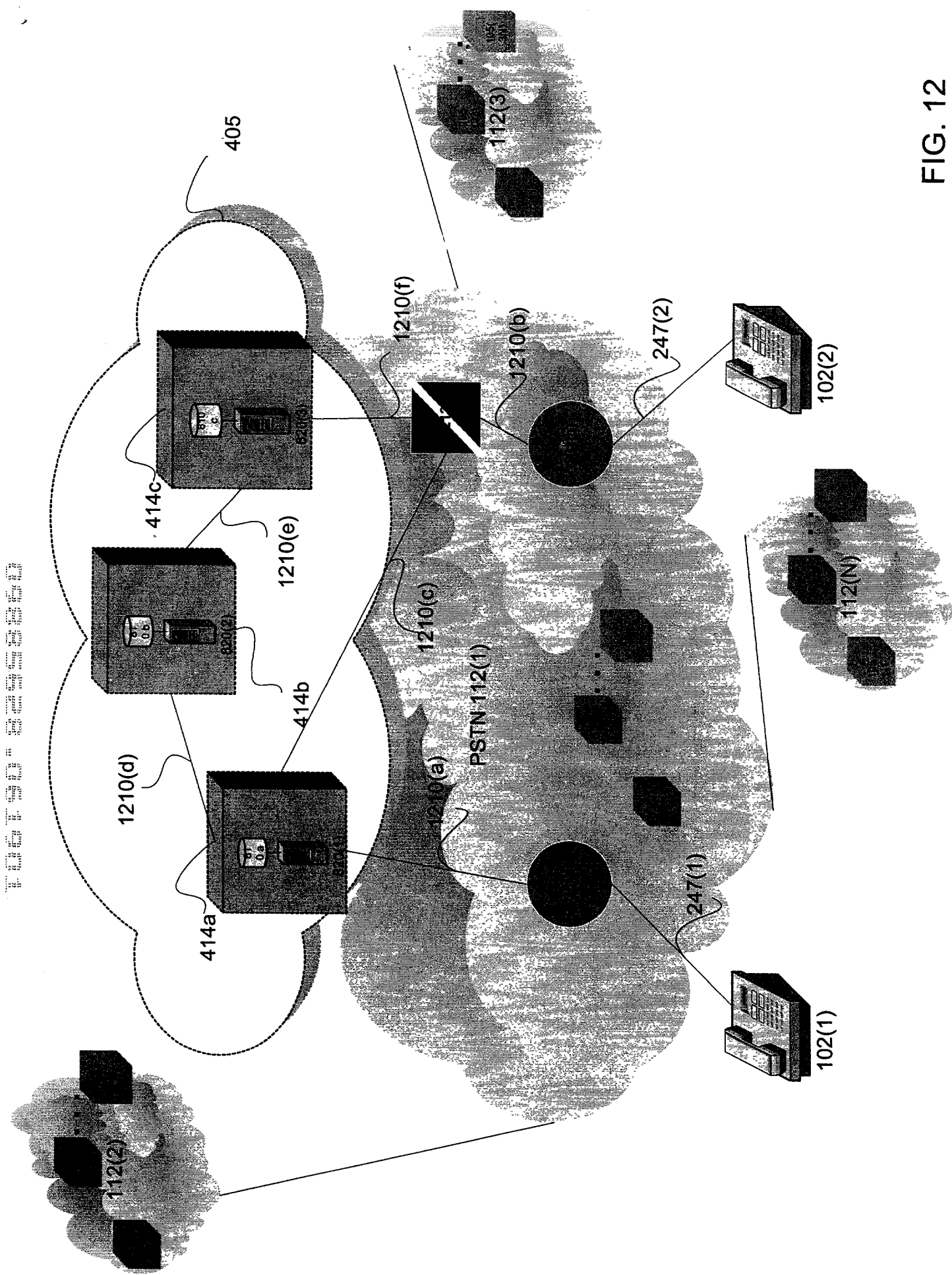


FIG. 12



FIG. 13

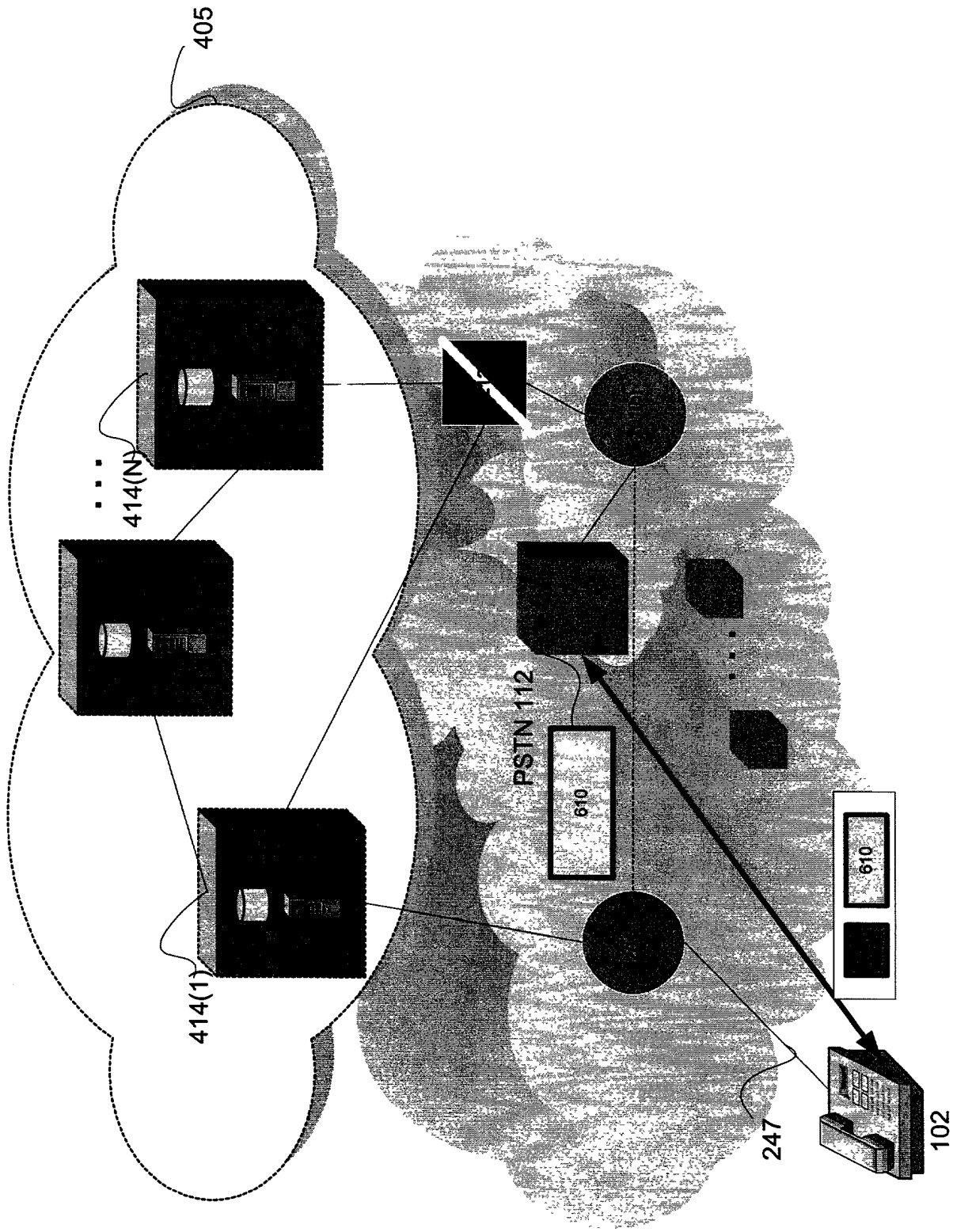


FIG. 13

FIG. 13

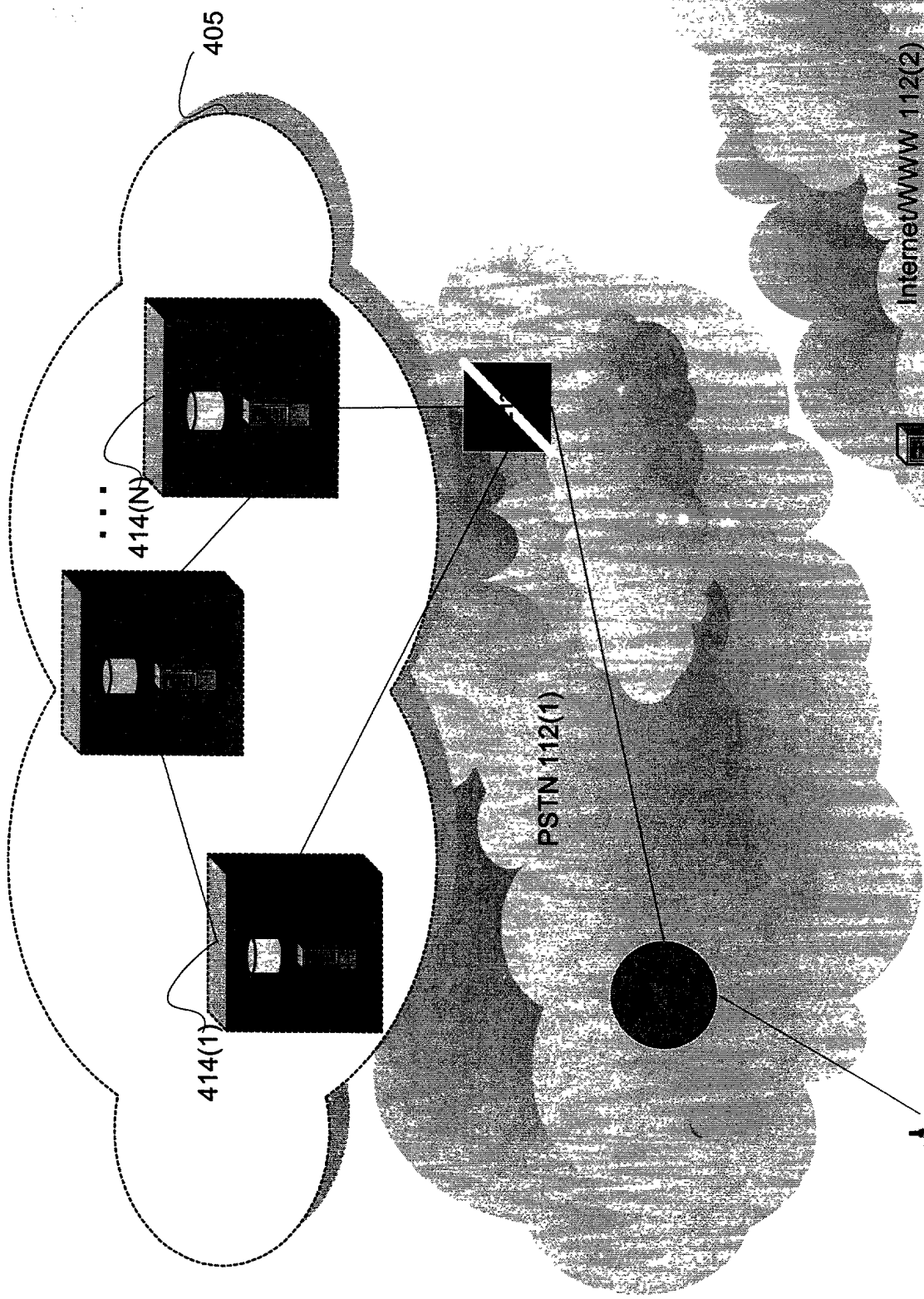
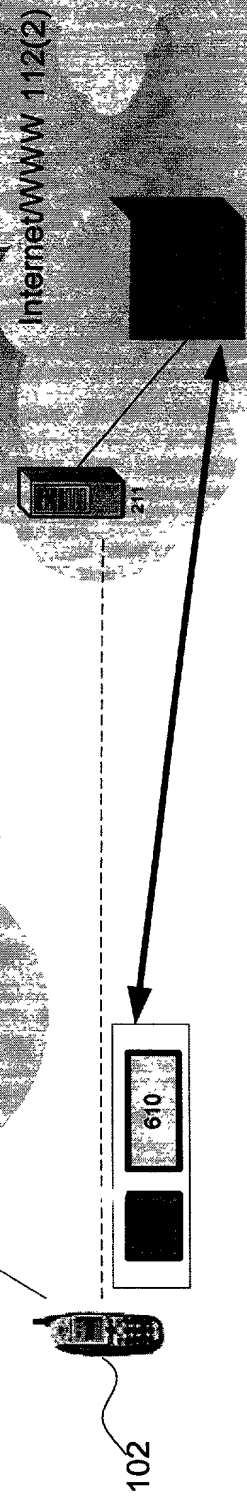


FIG. 14



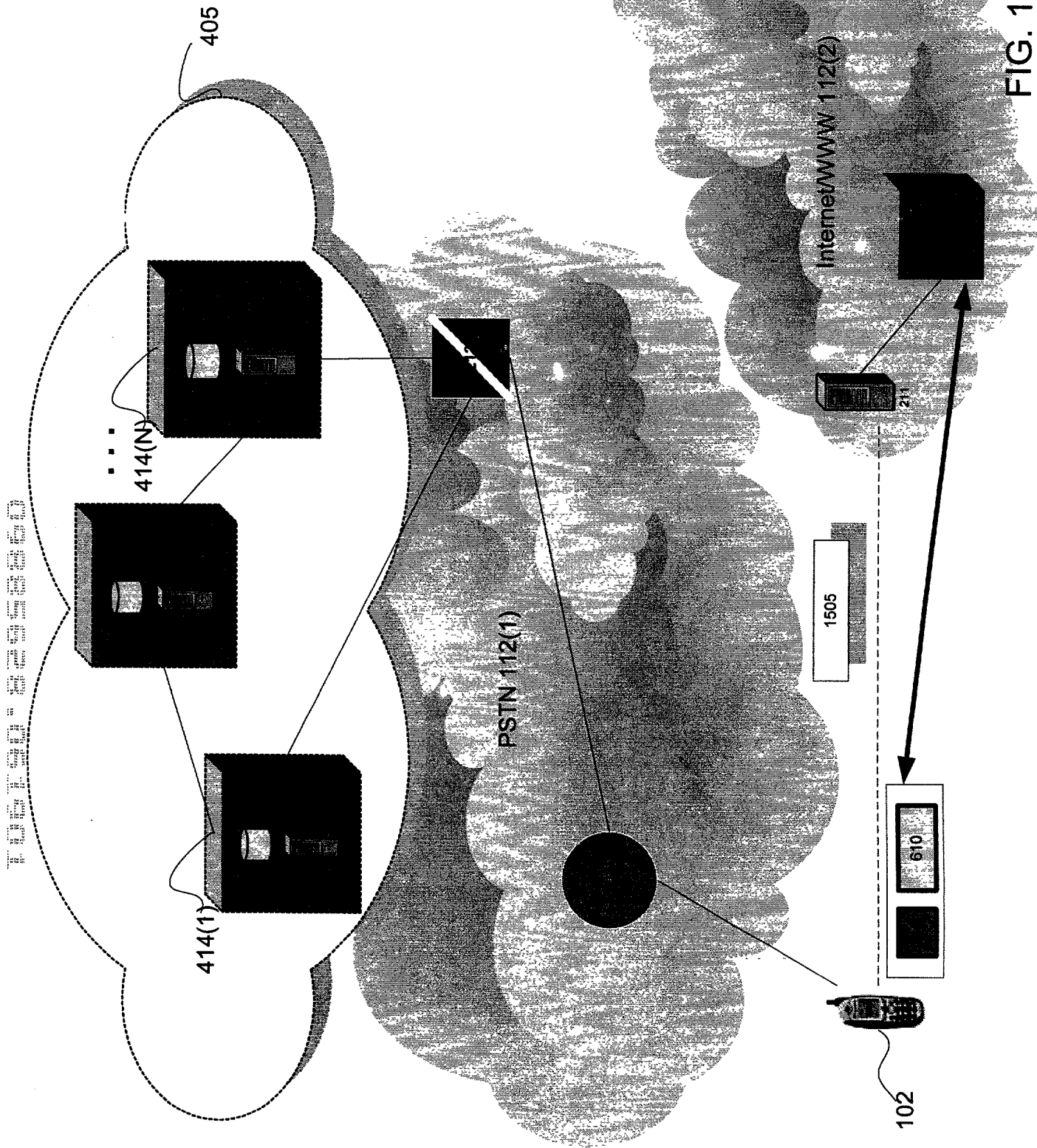


FIG. 15

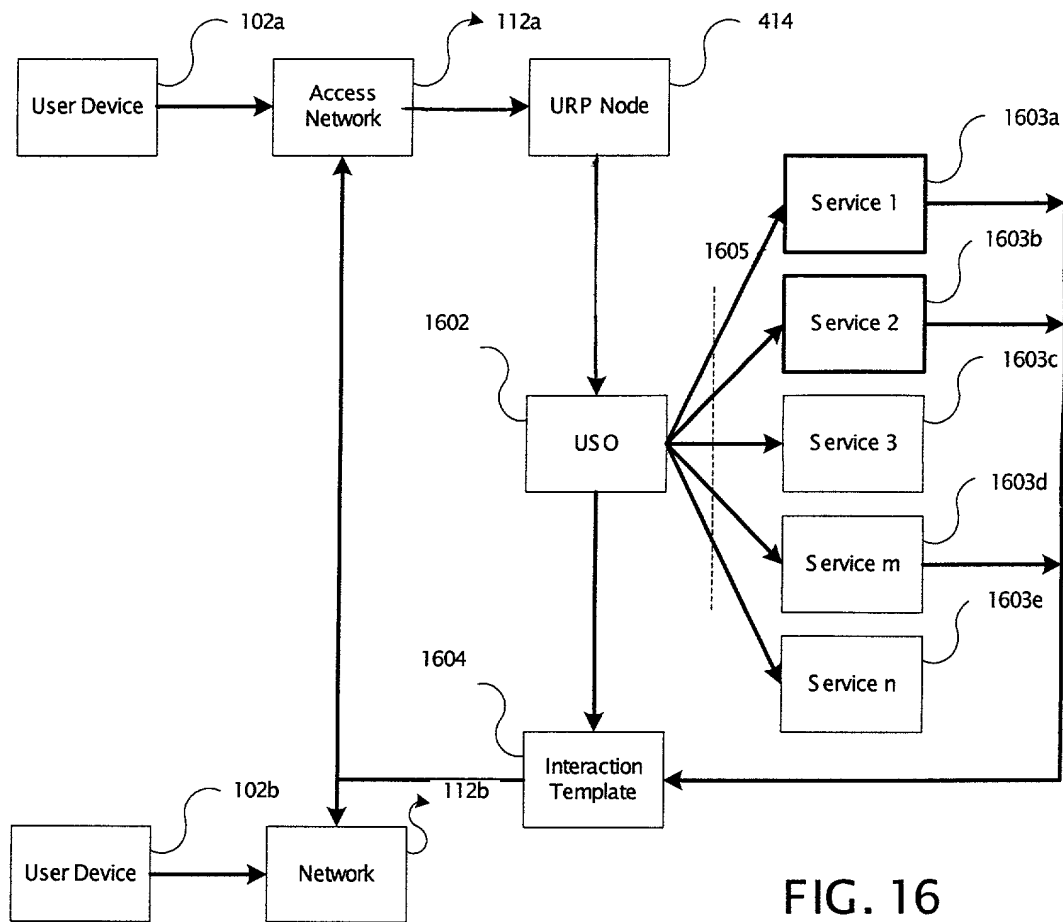


FIG. 16